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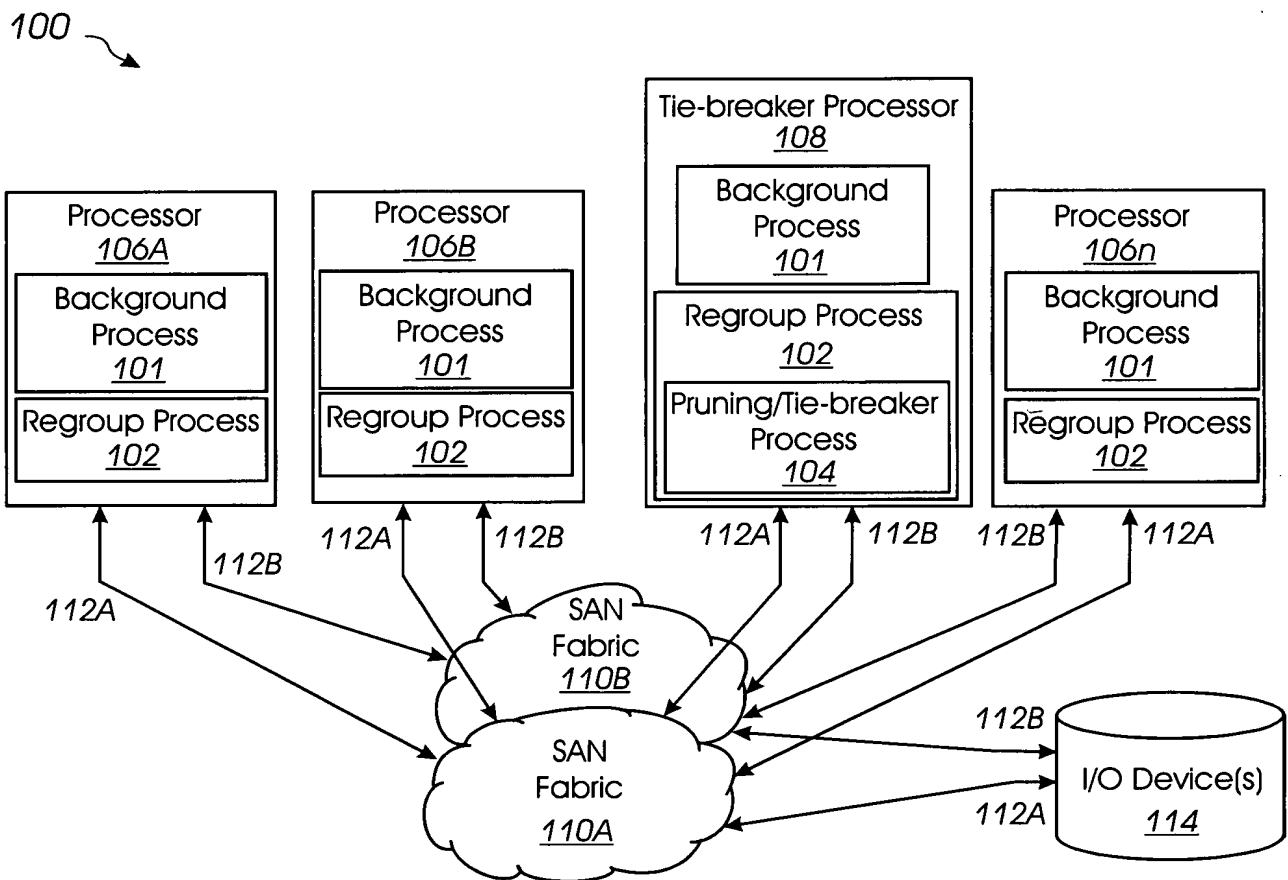


FIG. 1

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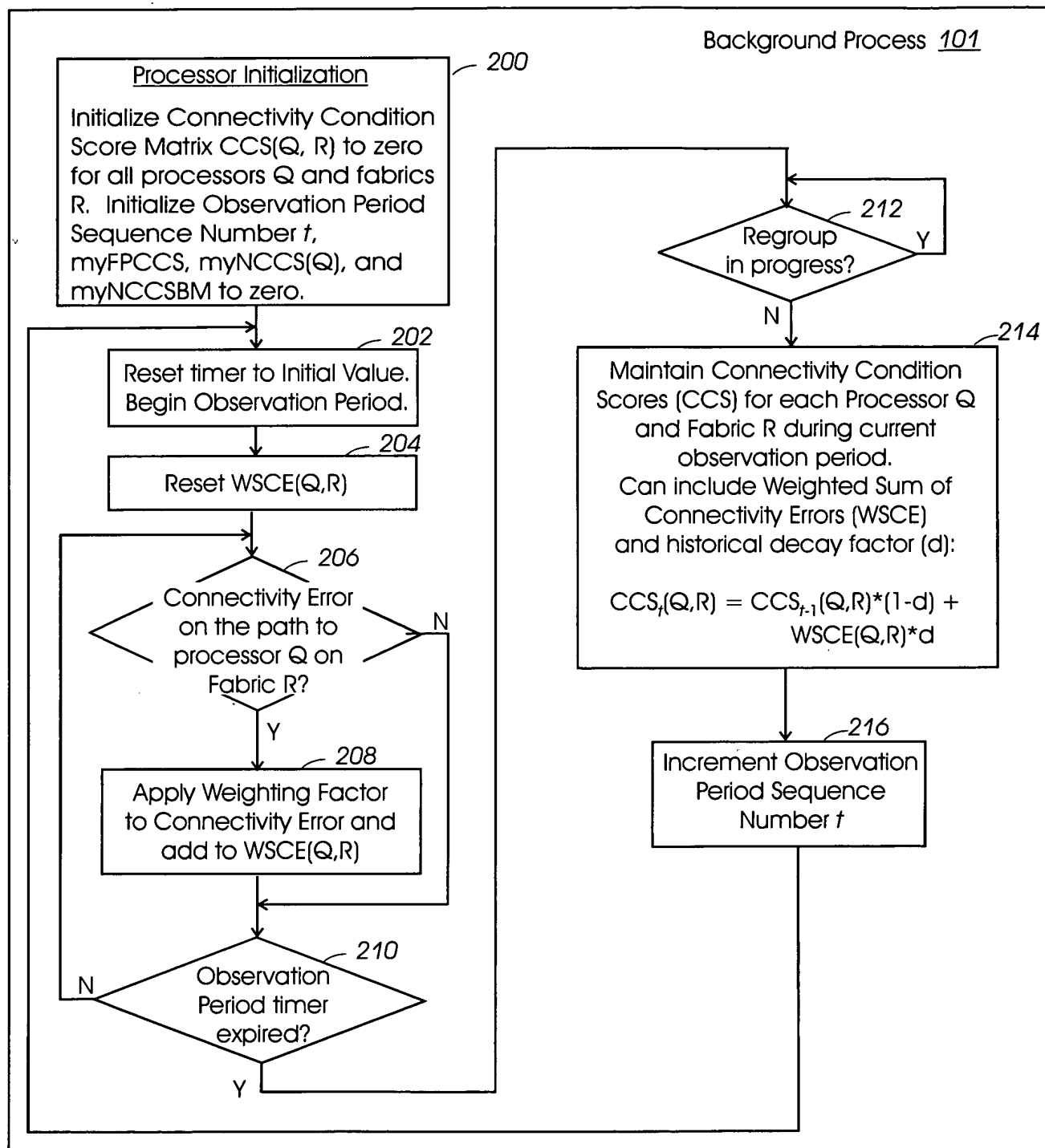
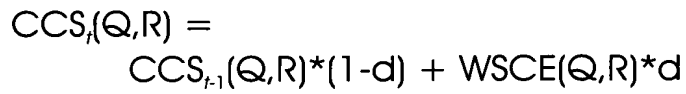


FIG. 2A



Error Type	Error Weight
Data Transfer Timeout	1
Barrier Timeout	2
.	.
.	.

The diagram illustrates the structure of the `myNCCS` and `myNCCSBM` bit mask. At the top, the formula $\text{myNCCS}(Q) = \sum_R \text{CCS}(Q, R) / \text{myFPCCS}$ is shown. Below it, a horizontal array of five boxes represents the `myNCCS` structure. The first box is labeled `myNCCS`, and the subsequent four boxes contain a dot (`.`). A bracket above the last four boxes is labeled `N Processors`. To the right of this array is the reference numeral 226. Below the `myNCCS` array, a horizontal array of ten vertical bars represents the `myNCCSBM` bit mask. The first bar is labeled `NCCSBM(Q)`, and the remaining nine bars are empty. A bracket above the last four bars of this array is labeled `myNCCSBM bit mask:`. To the right of this array is the reference numeral 228. Four lines connect the four dotted boxes in the `myNCCS` array to the four bars in the `myNCCSBM` bit mask array, indicating a mapping between the two structures.

FIG. 2B

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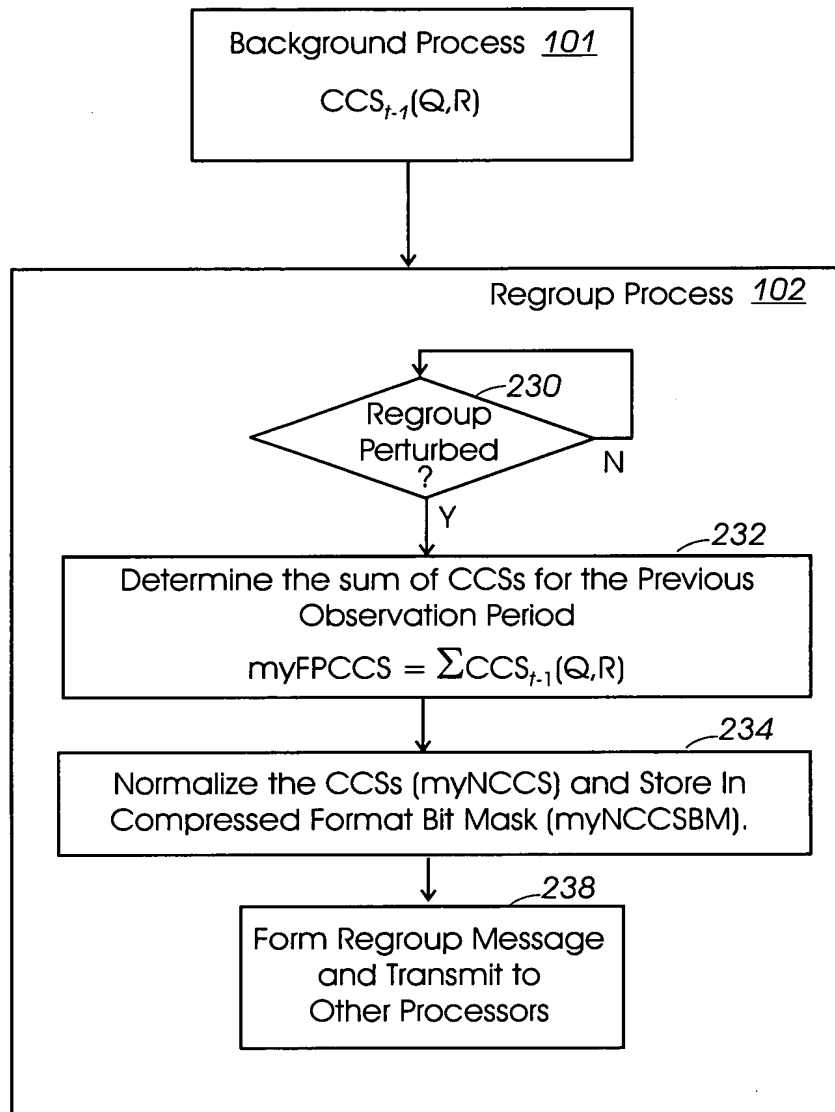


FIG. 2C

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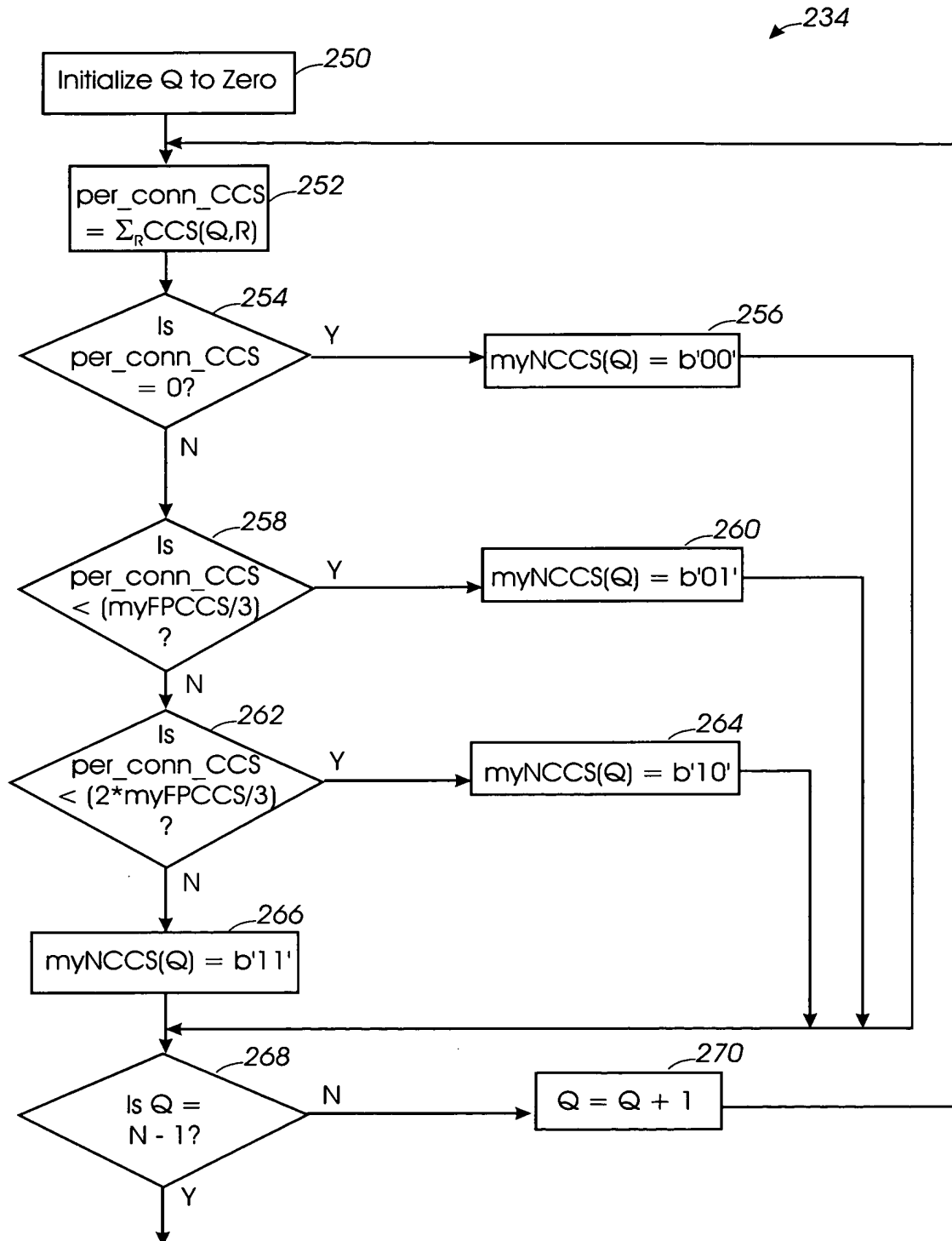
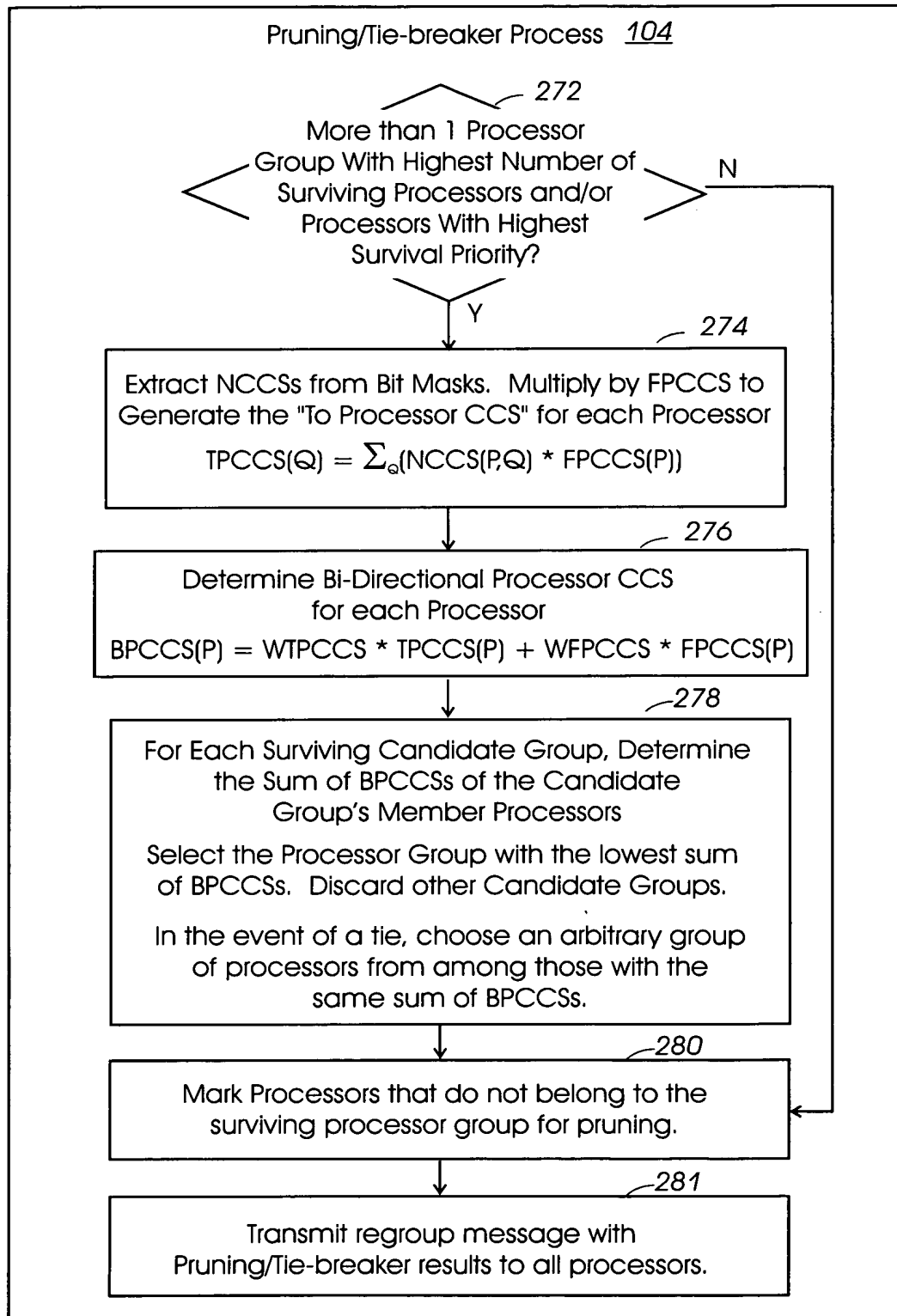


FIG. 2D

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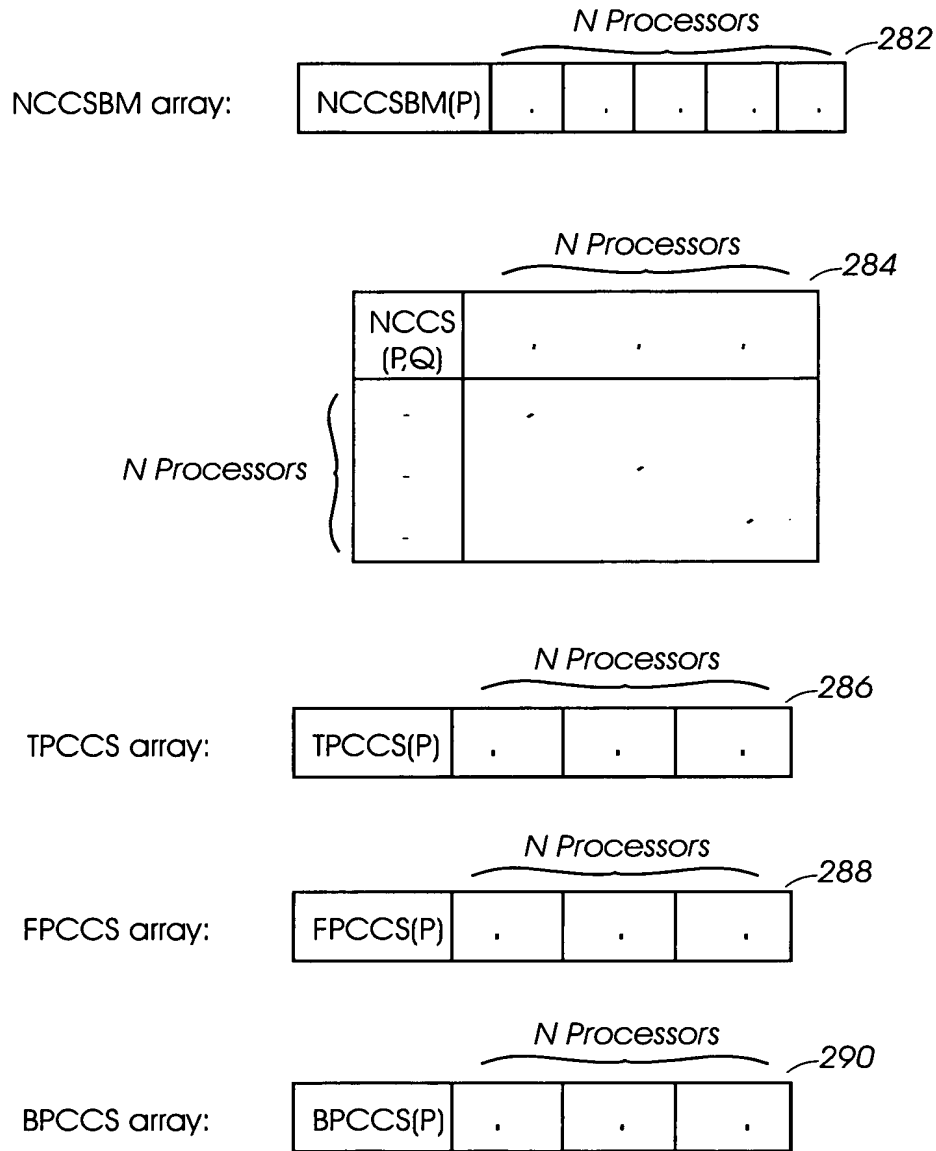


FIG. 2F

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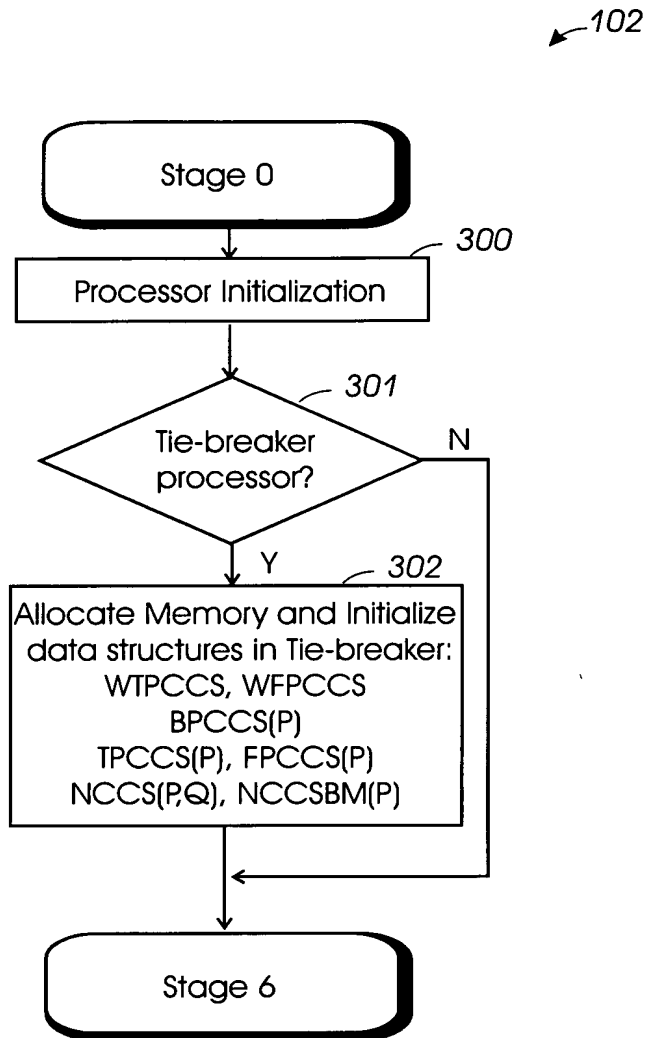


FIG. 3A

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